

#2/6/1/01
Date

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Mirmajid Seyyedy

Serial No.: Not yet assigned

Filed: February 9, 2001

For: FLIP CHIP TECHNIQUE FOR CHIP
ASSEMBLY

Examiner: Unknown

Group Art Unit: Unknown

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INFORMATION DISCLOSURE STATEMENT

Box PATENT APPLICATION
Commissioner for Patents
Washington, D.C. 20231


Sir:

The present application is a continuation of application Serial No. 08/788,209, filed January 24, 1997, pending.

Pursuant to M.P.E.P. 2001.06(b), the Examiner is respectfully requested to consider the information of record in the prior application, and to confirm in the first Office Action on the merits that such art has in fact been reviewed. A PTO-1449 form listing all of the information of record in the prior application is enclosed herewith.

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. 1.17(p) is required.

Respectfully submitted,



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Date: February 9, 2001

JRD/jml

Enclosure: Form PTO-1449

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